

Amendment to the Specification:

Please replace paragraph [0028] with the following amended paragraph:

[0028] Referencing FIG. 1, an integrated system 100, such as a programmable logic device (PLD), may comprise a plurality of transceivers 102-104 to be interfaced with an embedded programmable fabric 106. In one embodiment, programmable fabric 106 may comprise a portion of a field programmable gate array (FPGA). Processor core(s), such as processor core 110, can be optionally embedded inside programmable fabric 106. Interface layer 112 may facilitate communication between embedded processor core 110 and fabric 106. Further referencing FIG. 1, a pair of paths 114-115 may interface transceiver 102 to processor core 110 through interface layer 112. Likewise, in an alternative embodiment, the functionality associated with the embedded processor might just as easily be realized in the fabric of the programmable logic device.